

UTILITY PATENT APPLICATION TRANSMITTAL <i>(Only for new nonprovisional applications under 37 CFR 1.53(b))</i>	Attorney Docket No.	241677US2
	First Inventor or Application Identifier	
	Kenji YOSHIDA, et al.	
Title	METHOD OF ACCELERATING TEST OF SEMICONDUCTOR DEVICE	
	Assignee Name:	KABUSHIKI KAISHA TOSHIBA
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08/18/03

APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents		ADDRESS TO: Commissioner for Patents Mail Stop Patent Application Alexandria, Virginia 22313	
1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)		ACCOMPANYING APPLICATION PARTS <ul style="list-style-type: none"> 7. <input type="checkbox"/> Assignment Papers (cover sheet & document(s)) 8. <input checked="" type="checkbox"/> Application Data Sheet. See 37 CFR 1.76 9. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney (when there is an assignee) 10. <input type="checkbox"/> English Translation Document (if applicable) 11. <input type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input type="checkbox"/> Copies of IDS Citations 12. <input type="checkbox"/> Preliminary Amendment 13. <input checked="" type="checkbox"/> White Advance Serial No. Postcard 14. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) (1) (if foreign priority is claimed) 15. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27 16. <input checked="" type="checkbox"/> Other: Request for Priority 	
2. <input checked="" type="checkbox"/> Specification Total Sheets 35			
3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) Total Sheets 13			
4. <input type="checkbox"/> Oath or Declaration Total Pages <ul style="list-style-type: none"> a. <input type="checkbox"/> Newly executed (original or copy) b. <input type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed) <ul style="list-style-type: none"> i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). 			
5. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)			
6. <input type="checkbox"/> Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) <ul style="list-style-type: none"> a. <input type="checkbox"/> Computer Readable Form (CRF) b. Specification or Sequence Listing on : <ul style="list-style-type: none"> i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); or ii. <input type="checkbox"/> Paper c. <input type="checkbox"/> Statements verifying identity of above copies 			
17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below: <input type="checkbox"/> Continuation <input type="checkbox"/> Divisional <input type="checkbox"/> Continuation-in-part (CIP) of prior application no.:			
Prior application information: Examiner: Group Art Unit:			
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation <u>can only</u> be relied upon when a portion has been inadvertently omitted from the submitted application parts.			
18. Amend the specification by inserting before the first line the sentence: <input type="checkbox"/> This application is a <input type="checkbox"/> Continuation <input type="checkbox"/> Division <input type="checkbox"/> Continuation-in-part (CIP) of application Serial No. Filed on			
<input type="checkbox"/> This application claims priority of provisional application Serial No. Filed			
19. CORRESPONDENCE ADDRESS			
			
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Name:	Marvin J. Spivak	Registration No.:	24,913
Signature:	<i>Marvin J. Spivak</i>	Date:	8/18/03
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Registration Number 21,124

Docket No. 241677US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Kenji YOSHIDA, et al.

SERIAL NO: New Application

FILING DATE: Herewith

FOR: METHOD OF ACCELERATING TEST OF SEMICONDUCTOR DEVICE

FEE TRANSMITTAL

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	20 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	3 - 3 =	0	x \$84 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS (If applicable)			+ \$280 =	\$0.00
<input checked="" type="checkbox"/> LATE FILING OF DECLARATION			+ \$130 =	\$130.00
			BASIC FEE	\$750.00
			TOTAL OF ABOVE CALCULATIONS	\$880.00
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The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment form is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Date: 8/18/03


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